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Docket No.: M4065.0317/P317-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
David J. Corisis

Application No.: 10/043,104

Group Art Unit: 2823

Filed: January 14, 2002

Examiner: D. Collins

For: SEMICONDUCTOR PACKAGE

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

Applicant now responds to the restriction requirement set forth in the Office Action mailed May 16, 2003 (Paper No. 5). The Examiner has required restriction between:

Group I, claims 12-21, drawn to a semiconductor device, classified in class 257, subclass 678;

Group II, claims 22-26, drawn to a process of making a semiconductor device, classified in class 438, subclass 106; and

Group III, claim 27, drawn to an apparatus, classified in class 29, subclass 25.01.

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Applicant hereby elects Group I, claims 12-21 for continued examination,
without traverse.

Dated: June 2, 2003

Respectfully submitted,

By 

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